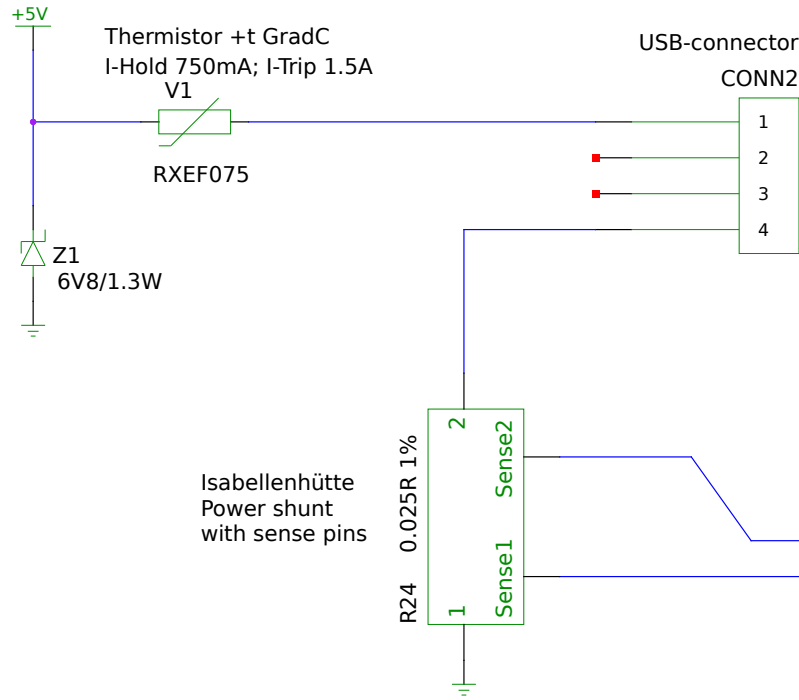


Controller with Reset Circuit		ZeroCAT Chipflasher	
TITLE		Flash free firmware, kick the Management Engine. git://zerocat.org/zerocat/projects/chipflasher	
FILE:	devkit-page01.sch	REVISION:	v2.1.5-0-d328367c9 (master)
PAGE	OF	DRAWN BY:	kmx



## USB Power

chips: 5V@100mA,  
use one USB-Port  
boards: 5V@500mA,  
use external Adapter

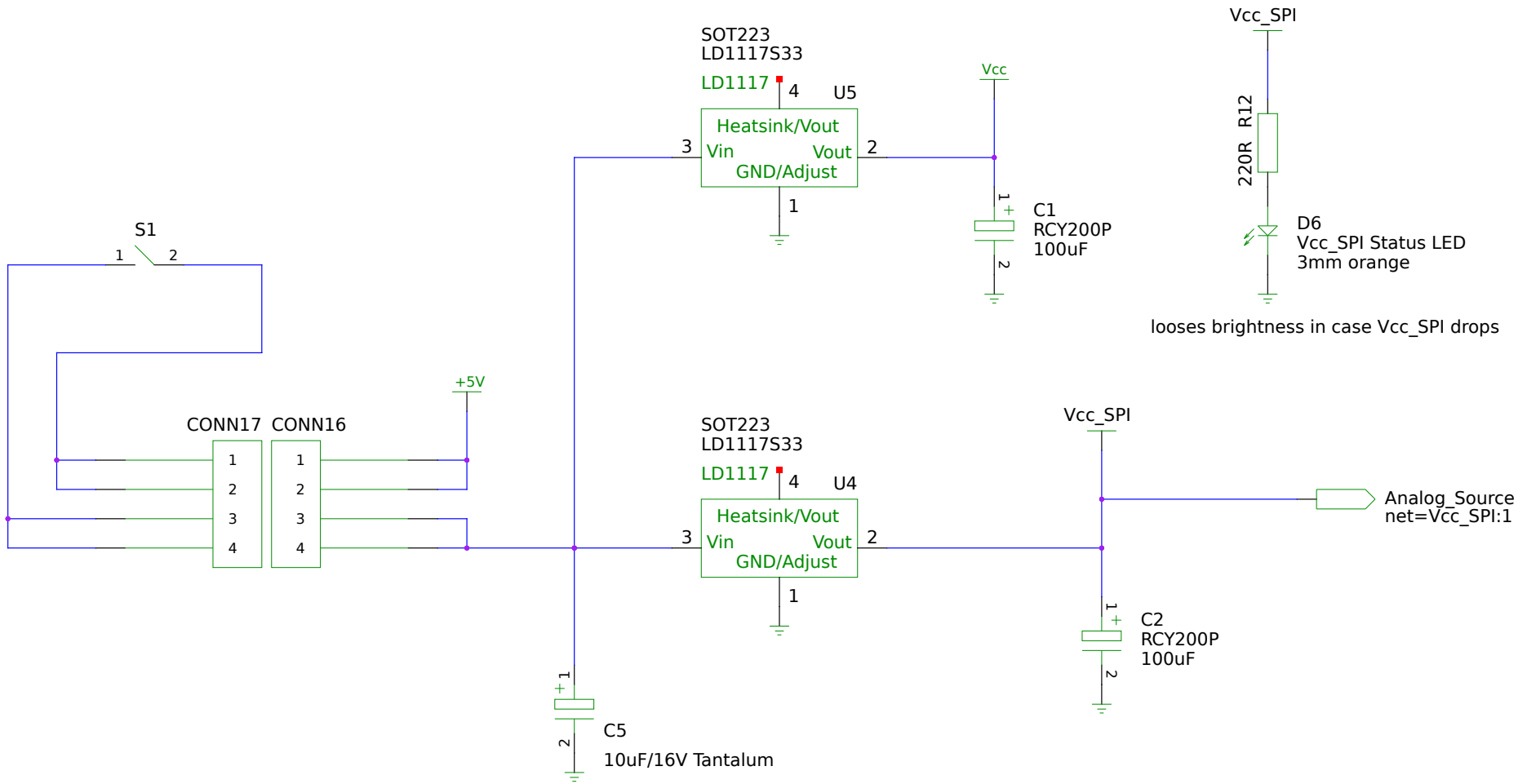
## GND-Sense

### Power Input with Sense

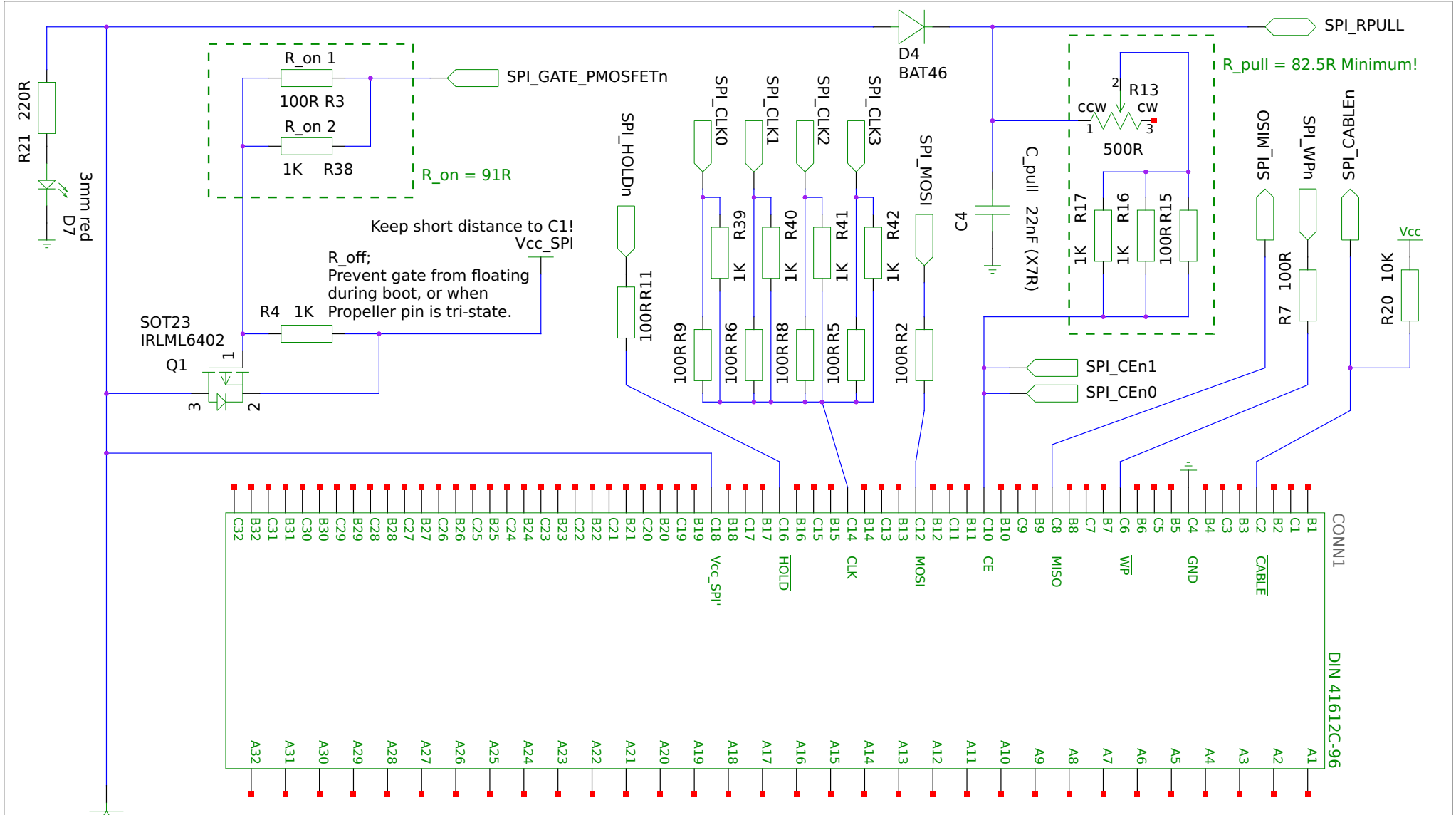
### Zerocat Chipflasher

Flash free firmware, kick the Management Engine.  
git://zerocat.org/zerocat/projects/chipflasher

TITLE			
FILE:	devkit-page02.sch	REVISION:	v2.1.5-0-d328367c9 (master)
PAGE	OF	DRAWN BY:	kmx

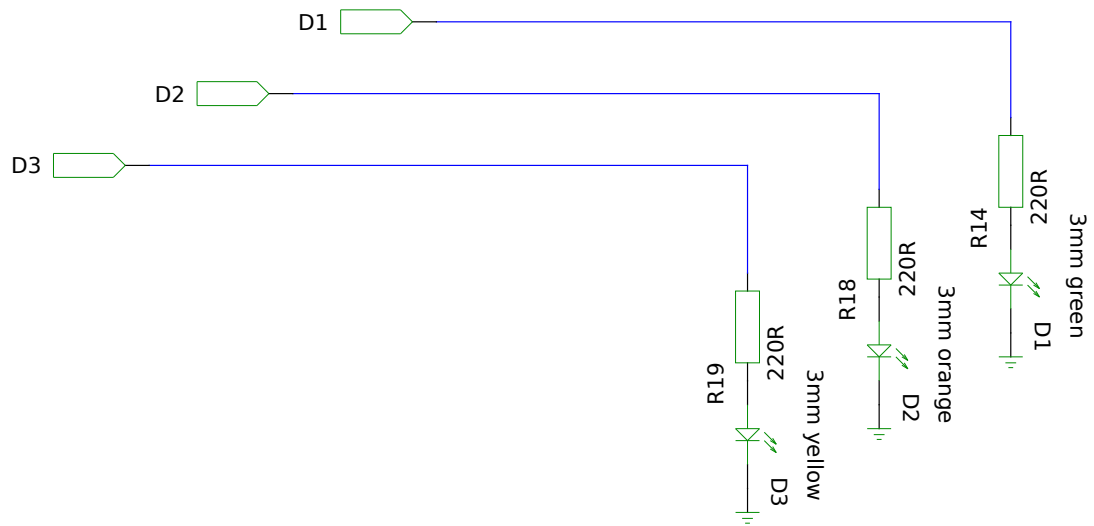


<b>Power Switch and Voltage Regulators</b>		<b>Zerocat Chipflasher</b>	
Flash free firmware, kick the Management Engine.		git://zerocat.org/zerocat/projects/chipflasher	
TITLE		REVISION:	v2.1.5-0-d328367c9 (master)
FILE:	devkit-page03.sch	DRAWN BY:	kmx
PAGE	OF		



D5 1N4148  
 prevent negative transients at load

<b>SPI Bus</b>		<b>ZeroCAT Chipflasher</b>	
TITLE		Flash free firmware, kick the Management Engine. <a href="https://zerocat.org/zerocat/projects/chipflasher">git://zerocat.org/zerocat/projects/chipflasher</a>	
FILE:	devkit-page04.sch	REVISION:	v2.1.5-0-d328367c9 (master)
PAGE	OF	DRAWN BY:	kmx



## Software Status LEDs

## Zerocat Chipflasher

Flash free firmware, kick the Management Engine.  
[git://zerocat.org/zerocat/projects/chipflasher](https://zerocat.org/zerocat/projects/chipflasher)

TITLE

FILE:

devkit-page05.sch

REVISION:

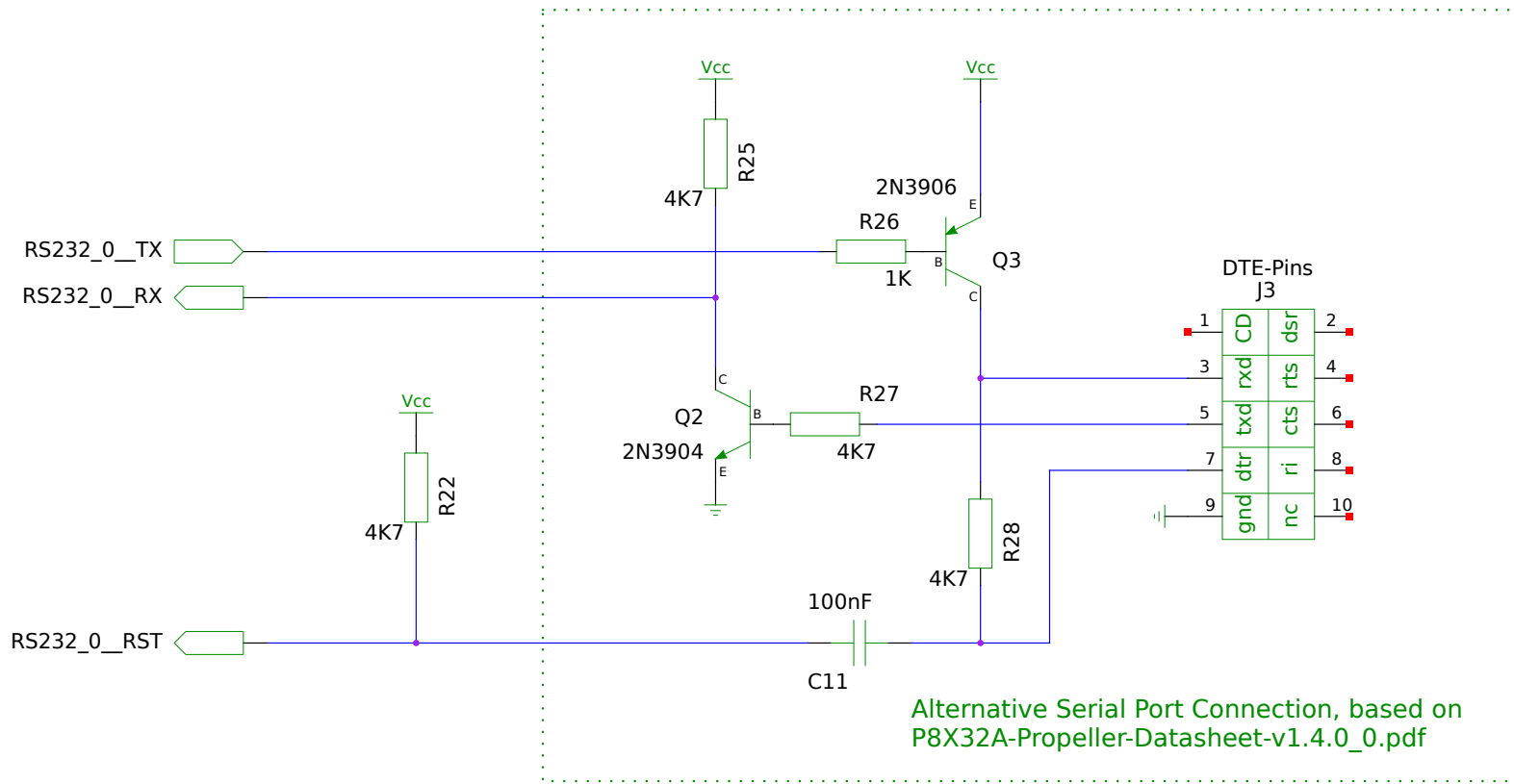
v2.1.5-0-d328367c9 (master)

PAGE

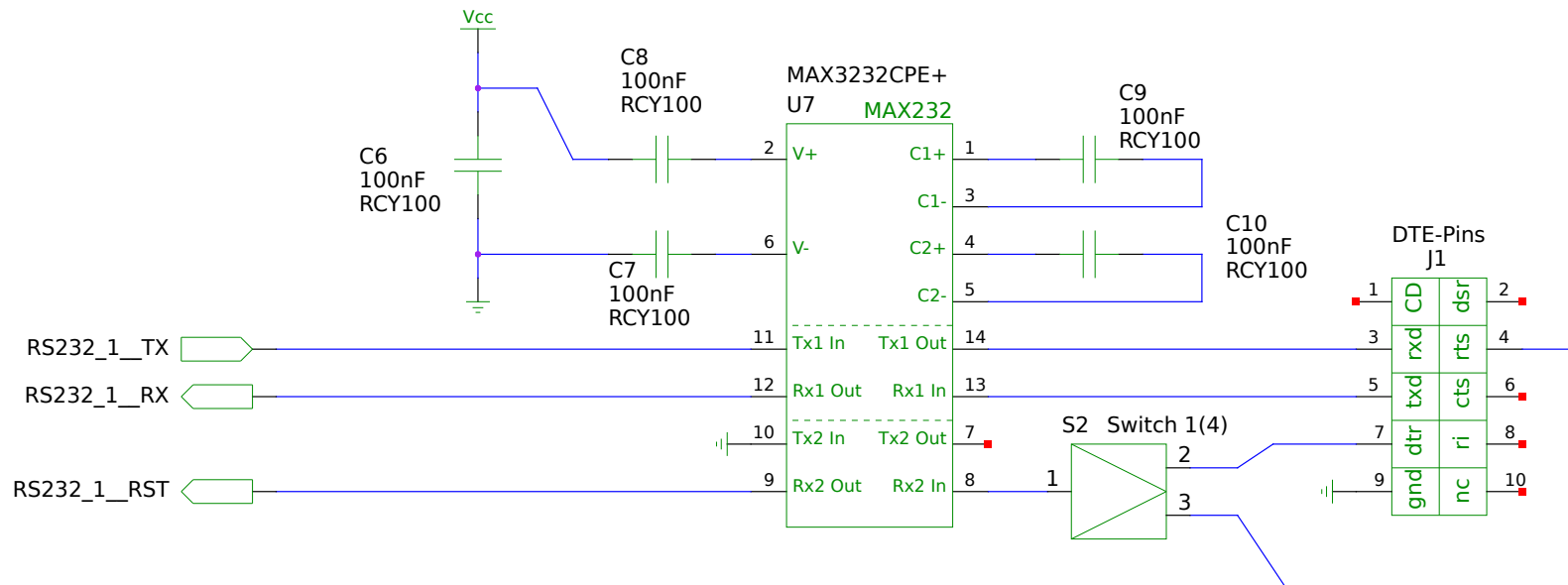
OF

DRAWN BY:

kmx



<b>RS232 Circuit #0</b>		<b>Zerocat Chipflasher</b>	
TITLE		Flash free firmware, kick the Management Engine. git://zerocat.org/zerocat/projects/chipflasher	
FILE:	devkit-page06.sch	REVISION:	v2.1.5-0-d328367c9 (master)
PAGE	OF	DRAWN BY:	kmx



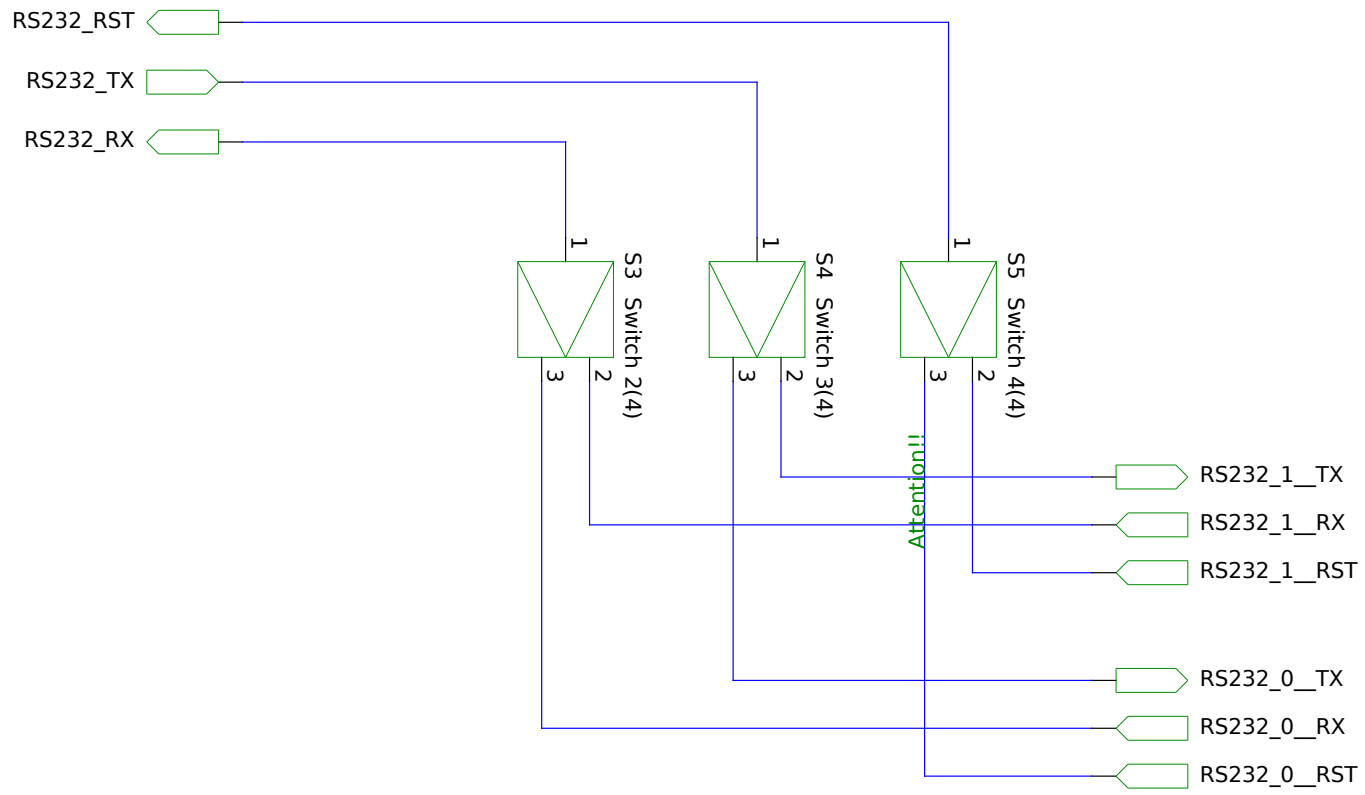
This circuit is generating RS232 voltages of about +/- 5.5VDC.

### RS232 Circuit #1

### Zerocat Chipflasher

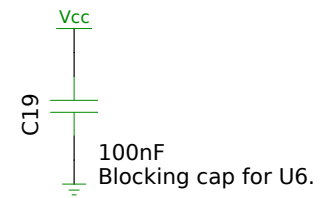
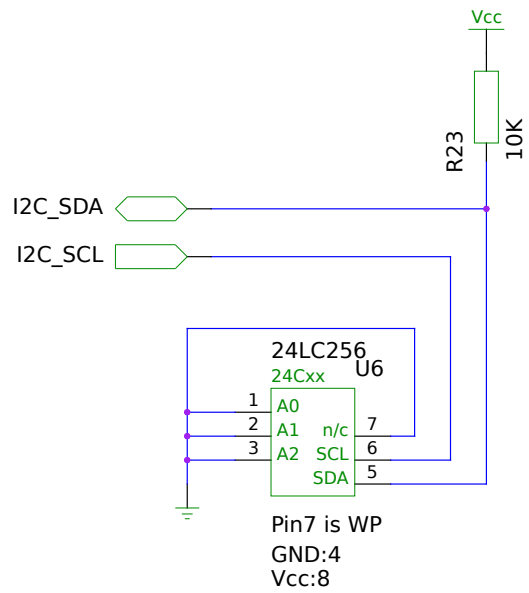
Flash free firmware, kick the Management Engine.  
[git://zerocat.org/zerocat/projects/chipflasher](https://zerocat.org/zerocat/projects/chipflasher)

TITLE		
FILE:	devkit-page07.sch	REVISION: v2.1.5-0-d328367c9 (master)
PAGE	OF	DRAWN BY: kmx



<b>RS232 Circuit Selector</b>		<b>Zerocat Chipflasher</b>	
TITLE		Flash free firmware, kick the Management Engine. git://zerocat.org/zerocat/projects/chipflasher	
FILE:	devkit-page08.sch	REVISION:	v2.1.5-0-d328367c9 (master)
PAGE	OF	DRAWN BY:	kmx





## Serial EEPROM of a non-free Design, optional Zerocat Chipflasher

Flash free firmware, kick the Management Engine.

[git://zerocat.org/zerocat/projects/chipflasher](https://zerocat.org/zerocat/projects/chipflasher)

TITLE		
FILE:	devkit-page09.sch	REVISION: v2.1.5-0-d328367c9 (master)
PAGE	OF	DRAWN BY: kmx

net=Vcc\_SPI:1  
Analog\_Source

Vcc  
R33  
200R

Divider Voltage Range:  
3.3V ... 2.35V

R34  
1K  
R32  
1K

3  
2  
1  
S6  
Switch 1(1)

R29  
2K2

R35  
300K  
R30  
300K

Vcc  
C20  
2.2nF (X7R)

C21  
2.2nF (X7R)

R31  
100K  
ADC\_OUT

ADC\_IN

ADC\_Calibration

Based on Parallax AN008-SigmaDeltaADC-v1.0.pdf

### Sigma-delta ADC Circuit

### Zerocat Chipflasher

Flash free firmware, kick the Management Engine.  
[git://zerocat.org/zerocat/projects/chipflasher](https://zerocat.org/zerocat/projects/chipflasher)

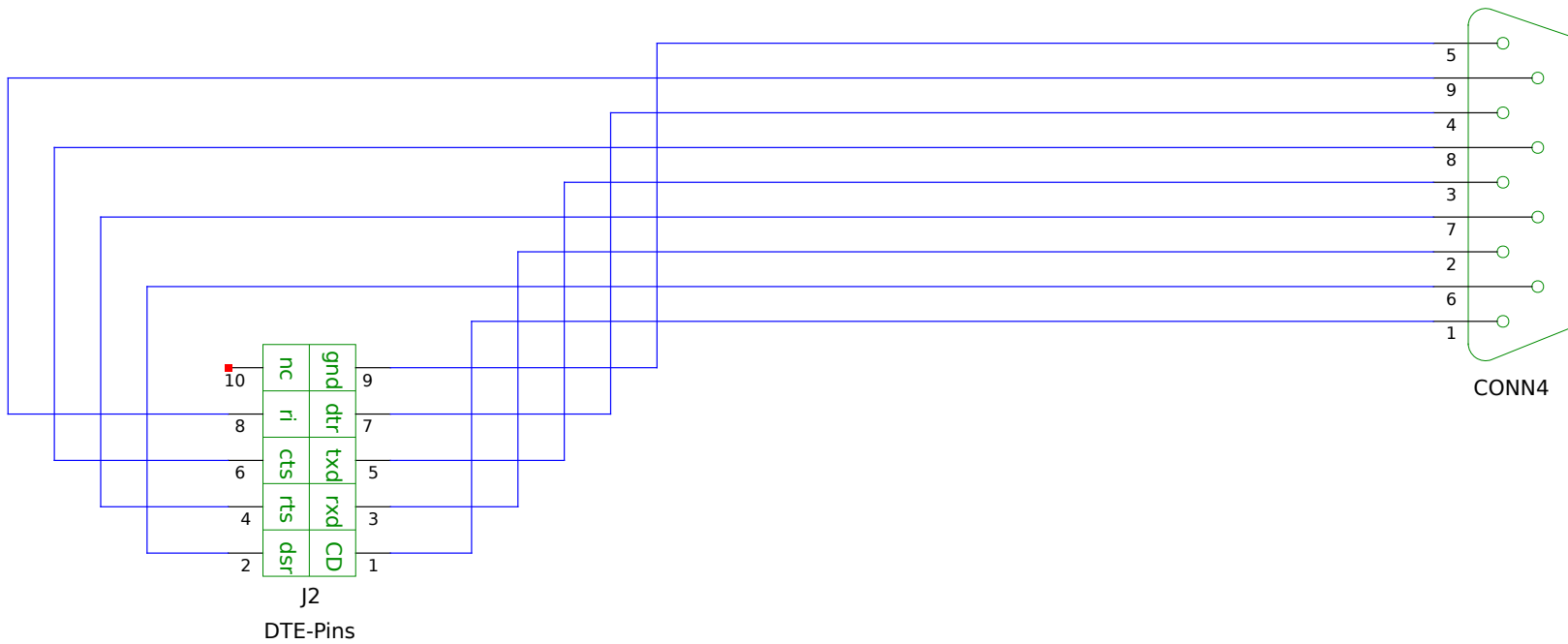
TITLE			
FILE:	devkit-page10.sch	REVISION:	v2.1.5-0-d328367c9 (master)
PAGE	OF	DRAWN BY:	kmx

Note:

DTE = Data Terminal Equipment (Laptop)

DCE = Data Communication Equipment (Propeller)

DTR = Data Terminal Ready (Laptop is ready to send/receive)



RS232 Cable

Zerocat Chipflasher

Flash free firmware, kick the Management Engine.  
[git://zerocat.org/zerocat/projects/chipflasher](https://zerocat.org/zerocat/projects/chipflasher)

TITLE

FILE:

devkit-page11.sch

REVISION:

v2.1.5-0-d328367c9 (master)

PAGE

OF

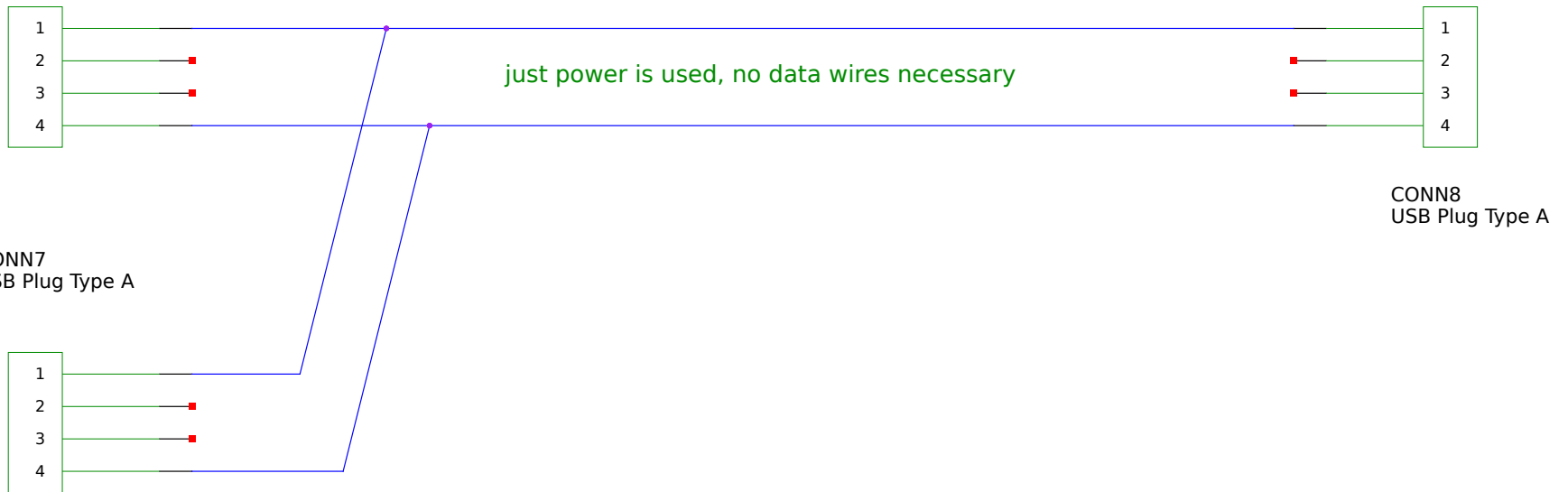
DRAWN BY:

kmx

CONN6  
USB Plug Type A

Host

Chipflasher

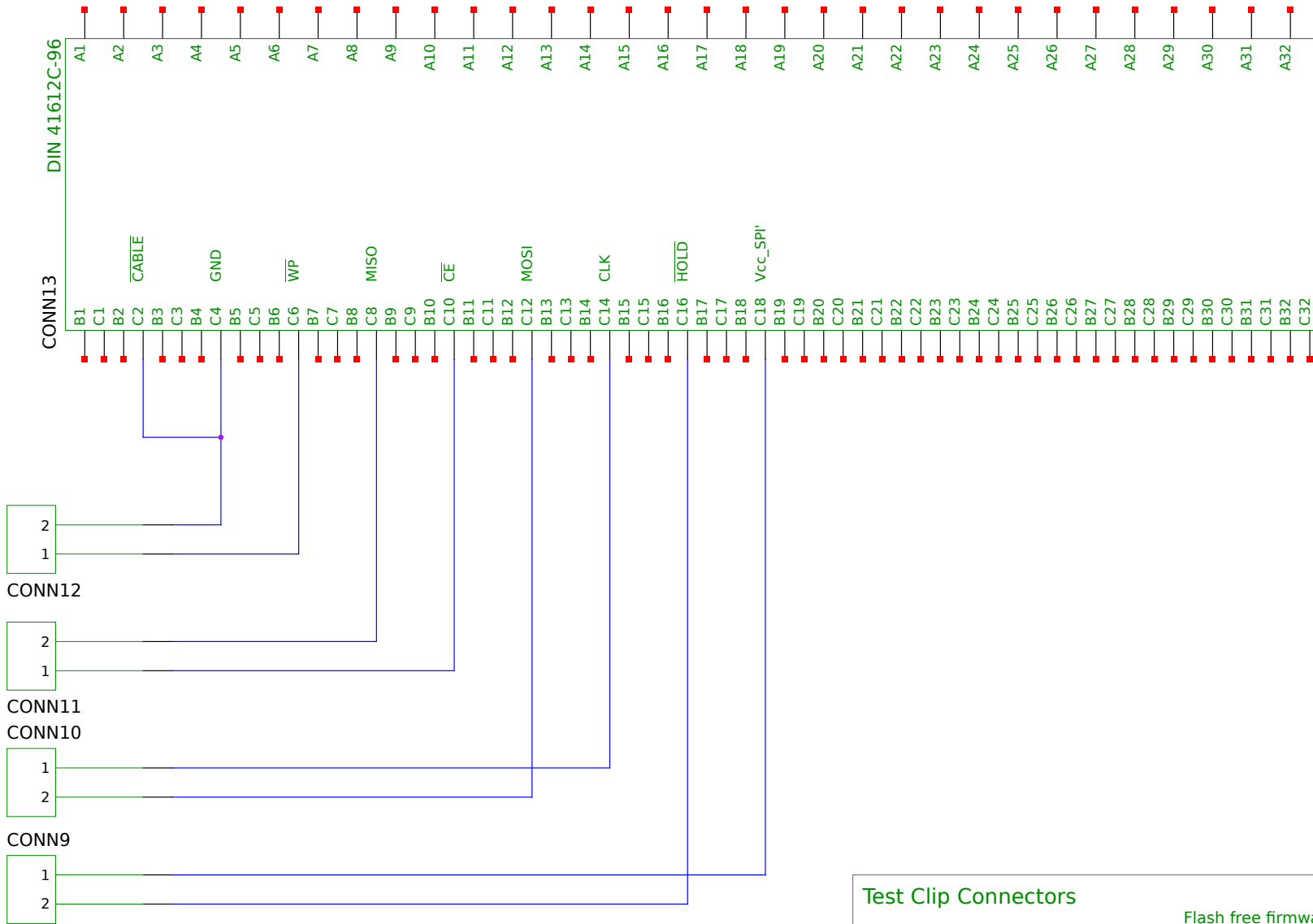


### Y-USB Power Cable

### Zerocat Chipflasher

Flash free firmware, kick the Management Engine.  
[git://zerocat.org/zerocat/projects/chipflasher](https://zerocat.org/zerocat/projects/chipflasher)

TITLE			
FILE:	devkit-page12.sch	REVISION:	v2.1.5-0-d328367c9 (master)
PAGE	OF	DRAWN BY:	kmx

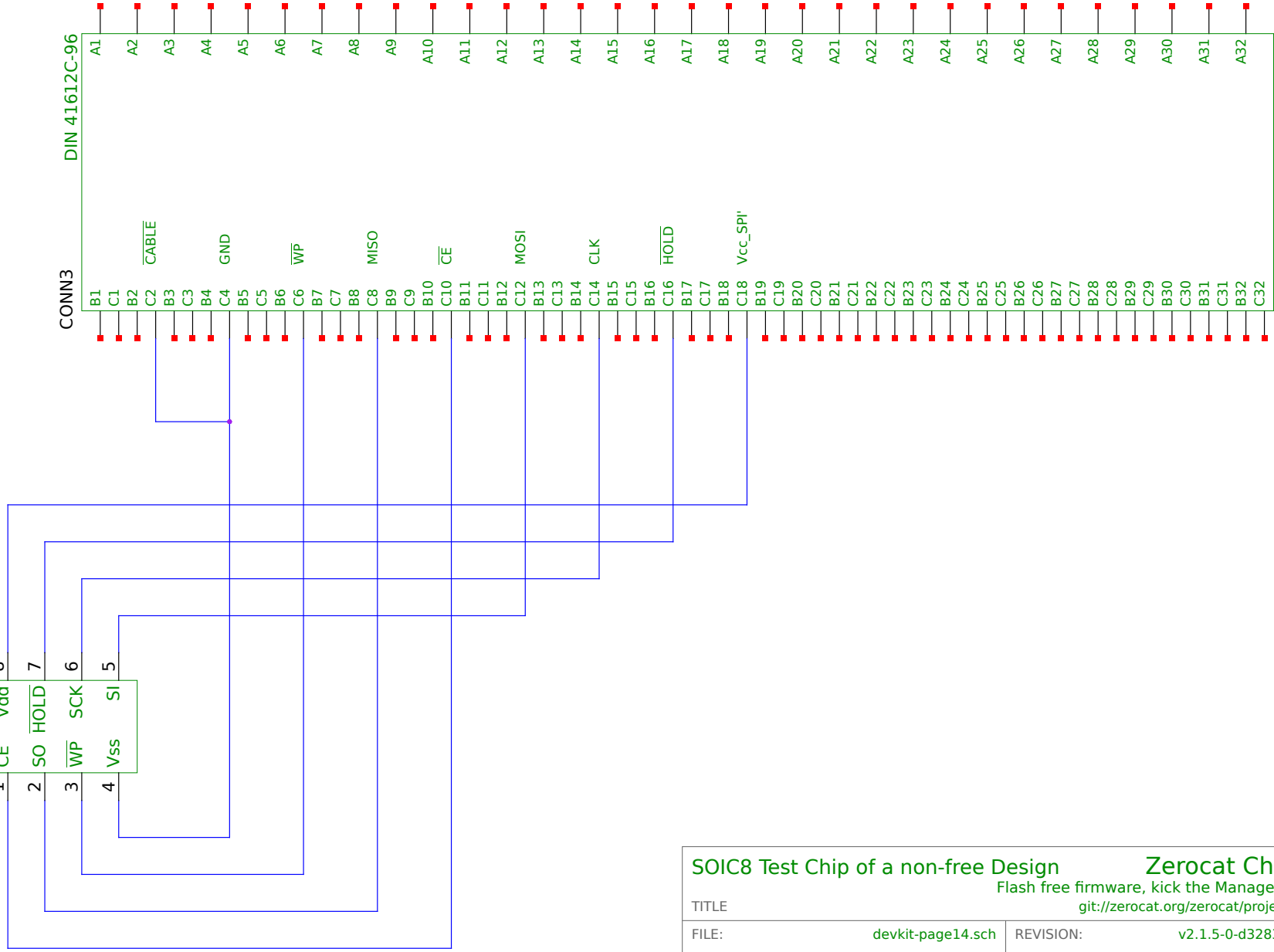
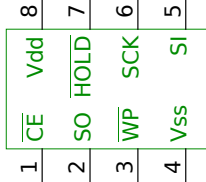


<b>Test Clip Connectors</b>		<b>Zerocat Chipflasher</b>	
TITLE		Flash free firmware, kick the Management Engine. git://zerocat.org/zerocat/projects/chipflasher	
FILE:	devkit-page13.sch	REVISION:	v2.1.5-0-d328367c9 (master)
PAGE	OF	DRAWN BY:	kmx

# SOIC8

Chips with same Pinout:  
M95020W  
MX25L1605D

SST25VF016B U8



## SOIC8 Test Chip of a non-free Design

## Zerocat Chipflasher

Flash free firmware, kick the Management Engine.

[git://zerocat.org/projects/chipflasher](https://zerocat.org/projects/chipflasher)

TITLE

FILE:

devkit-page14.sch

REVISION:

v2.1.5-0-d328367c9 (master)

PAGE

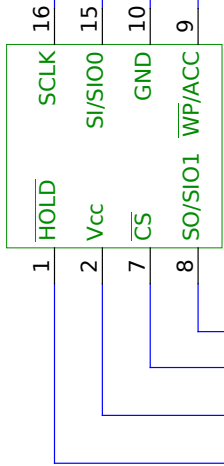
OF

DRAWN BY:

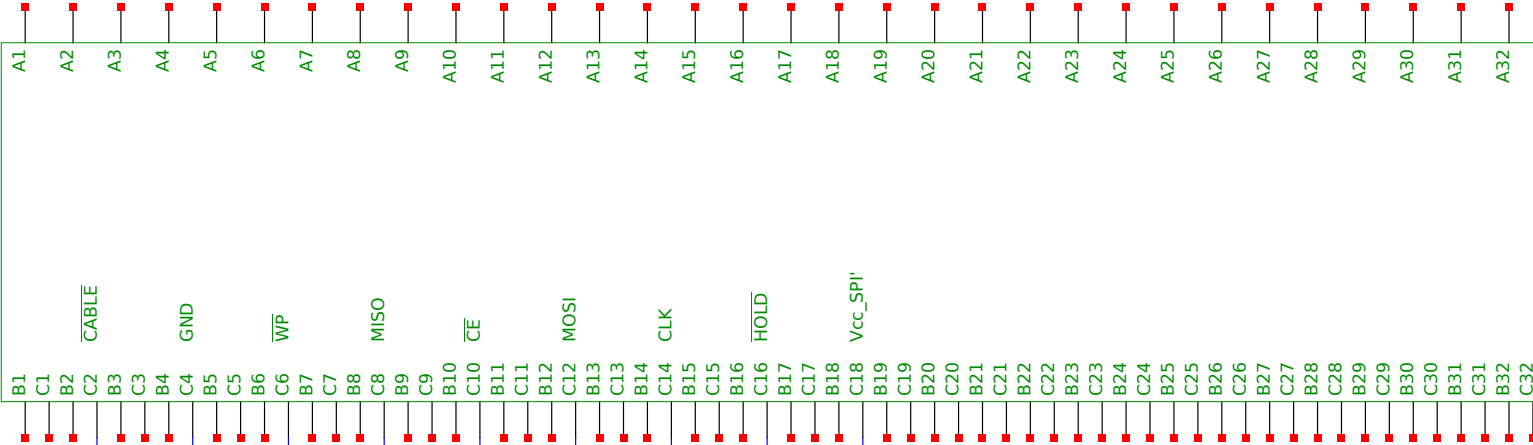
kmx

# SOIC16

MX25L6405D U10



CONN5  
DIN 41612C-96



## SOIC16 Test Chip of a non-free Design Zerocat Chipflasher

Flash free firmware, kick the Management Engine.  
[git://zerocat.org/zerocat/projects/chipflasher](https://zerocat.org/zerocat/projects/chipflasher)

TITLE		
FILE:	devkit-page15.sch	REVISION: v2.1.5-0-d328367c9 (master)
PAGE	OF	DRAWN BY: kmx