

## USB Power

chips: 5V@100mA,  
use one USB-Port  
boards: 5V@500mA,  
use external Adapter

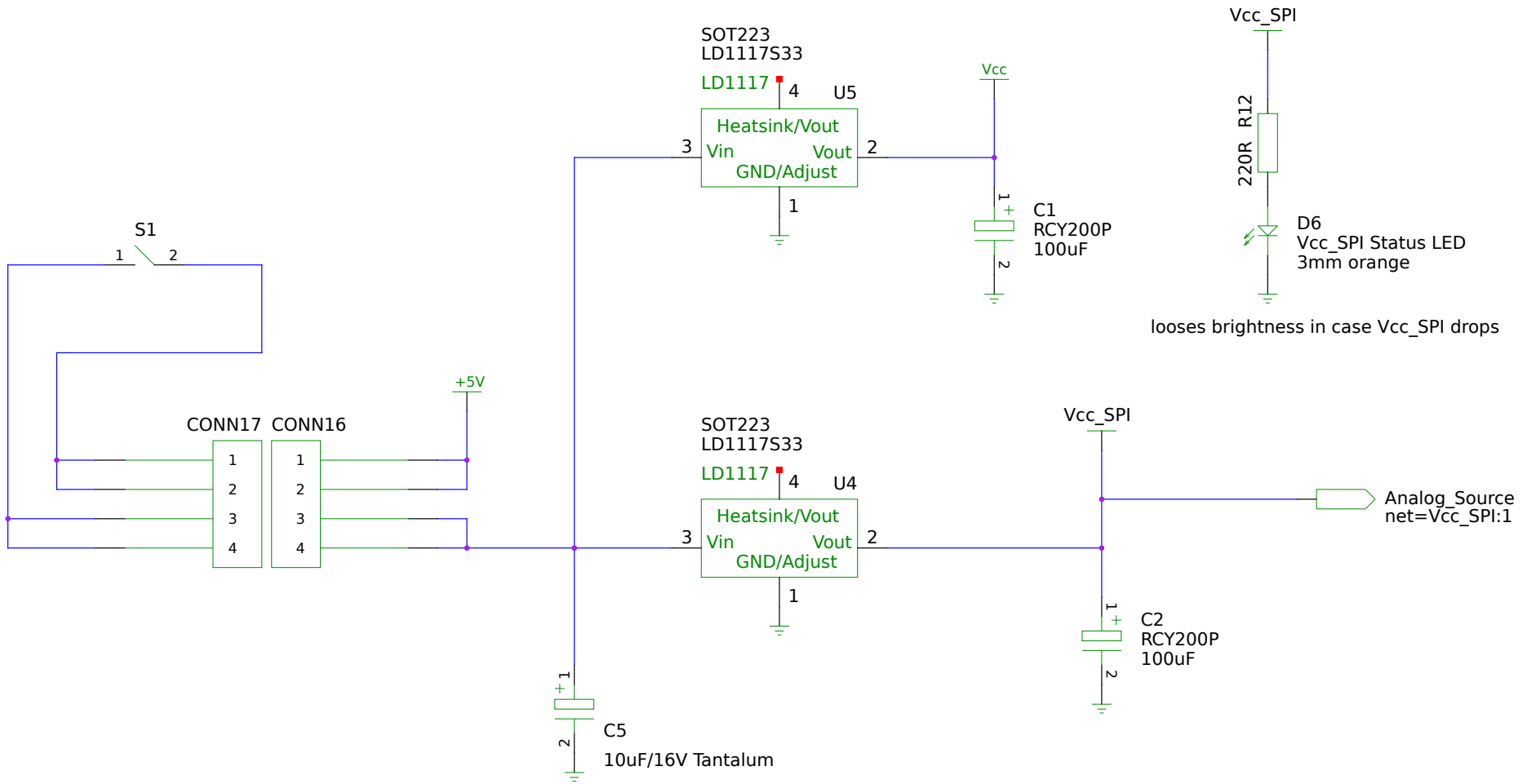
## GND-Sense

### Power Input with Sense

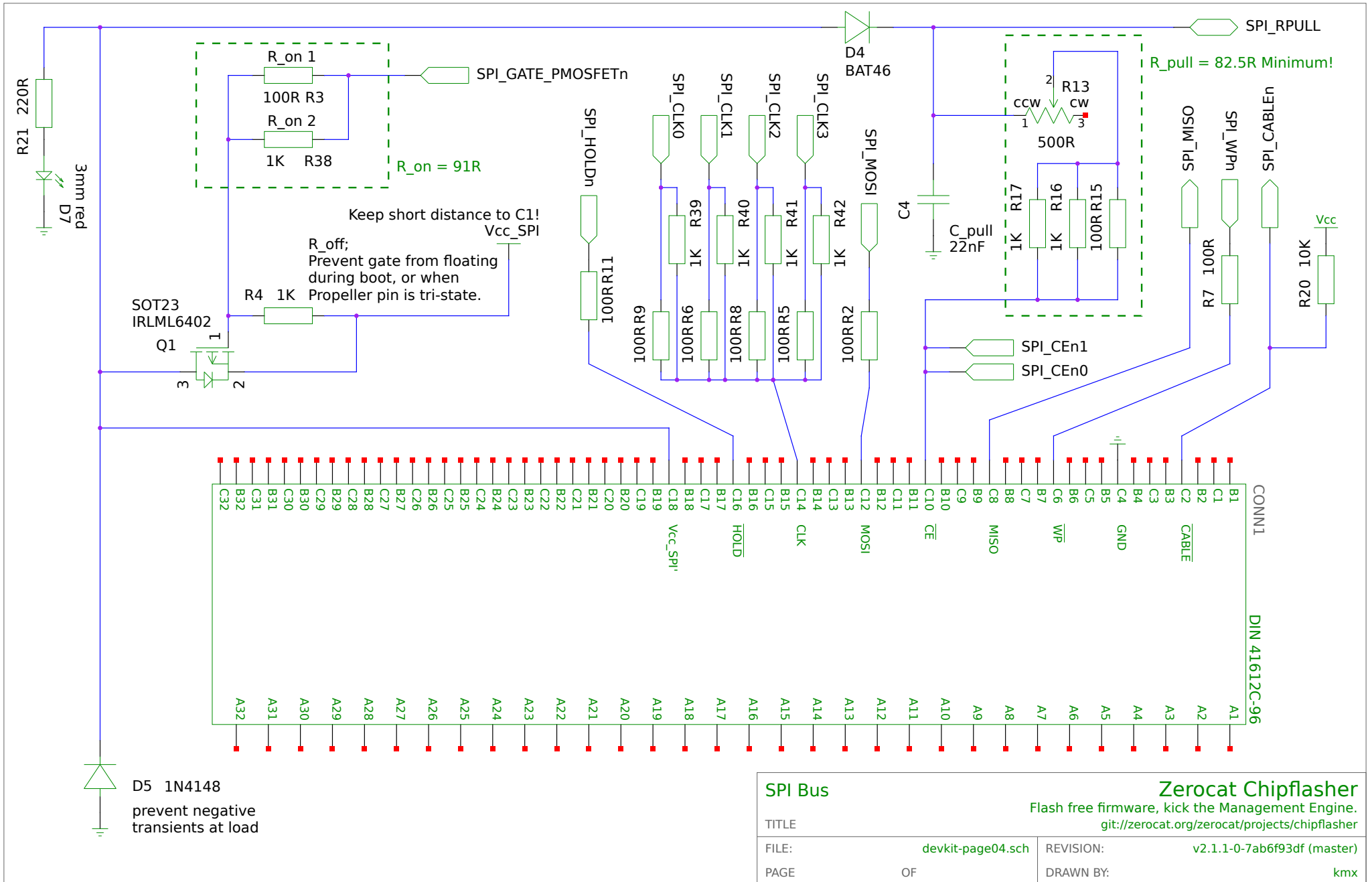
### Zerocat Chipflasher

Flash free firmware, kick the Management Engine.  
[git://zerocat.org/zerocat/projects/chipflasher](https://zerocat.org/zerocat/projects/chipflasher)

TITLE			
FILE:	devkit-page02.sch	REVISION:	v2.1.1-0-7ab6f93df (master)
PAGE	OF	DRAWN BY:	kmx



<b>Power Switch and Voltage Regulators</b>		<b>ZeroCAT Chipflasher</b>	
Flash free firmware, kick the Management Engine.		git://zerocat.org/zerocat/projects/chipflasher	
TITLE		REVISION:	v2.1.1-0-7ab6f93df (master)
FILE:	devkit-page03.sch	DRAWN BY:	kmx
PAGE	OF		

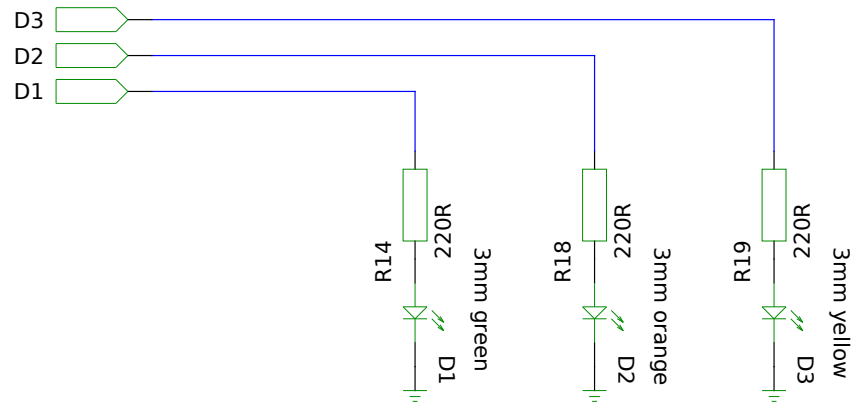


D5 1N4148  
prevent negative transients at load

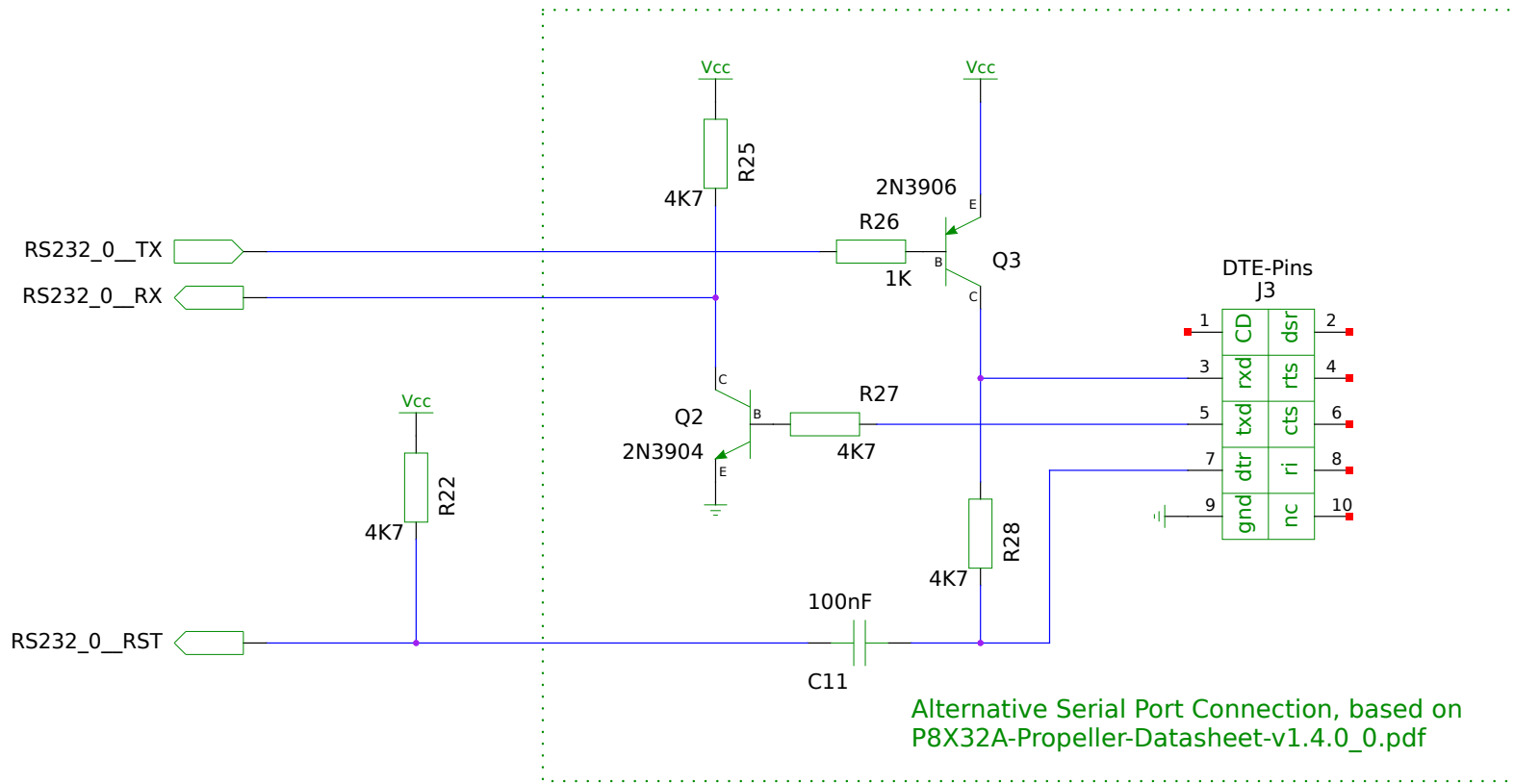
Keep short distance to C1!  
Vcc\_SPI

R\_off;  
Prevent gate from floating during boot, or when Propeller pin is tri-state.

R\_pull = 82.5R Minimum!

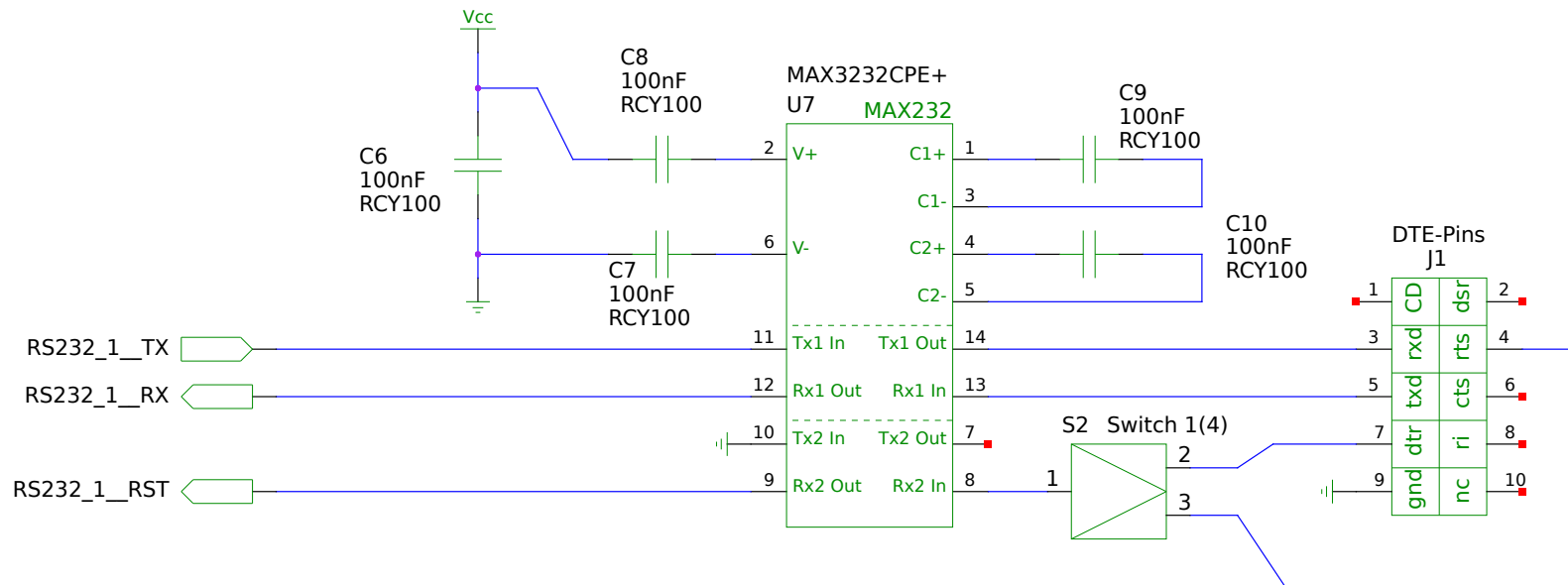


<b>Software Status LEDs</b>		<b>Zerocat Chipflasher</b>	
TITLE		Flash free firmware, kick the Management Engine. git://zerocat.org/zerocat/projects/chipflasher	
FILE:	devkit-page05.sch	REVISION:	v2.1.1-0-7ab6f93df (master)
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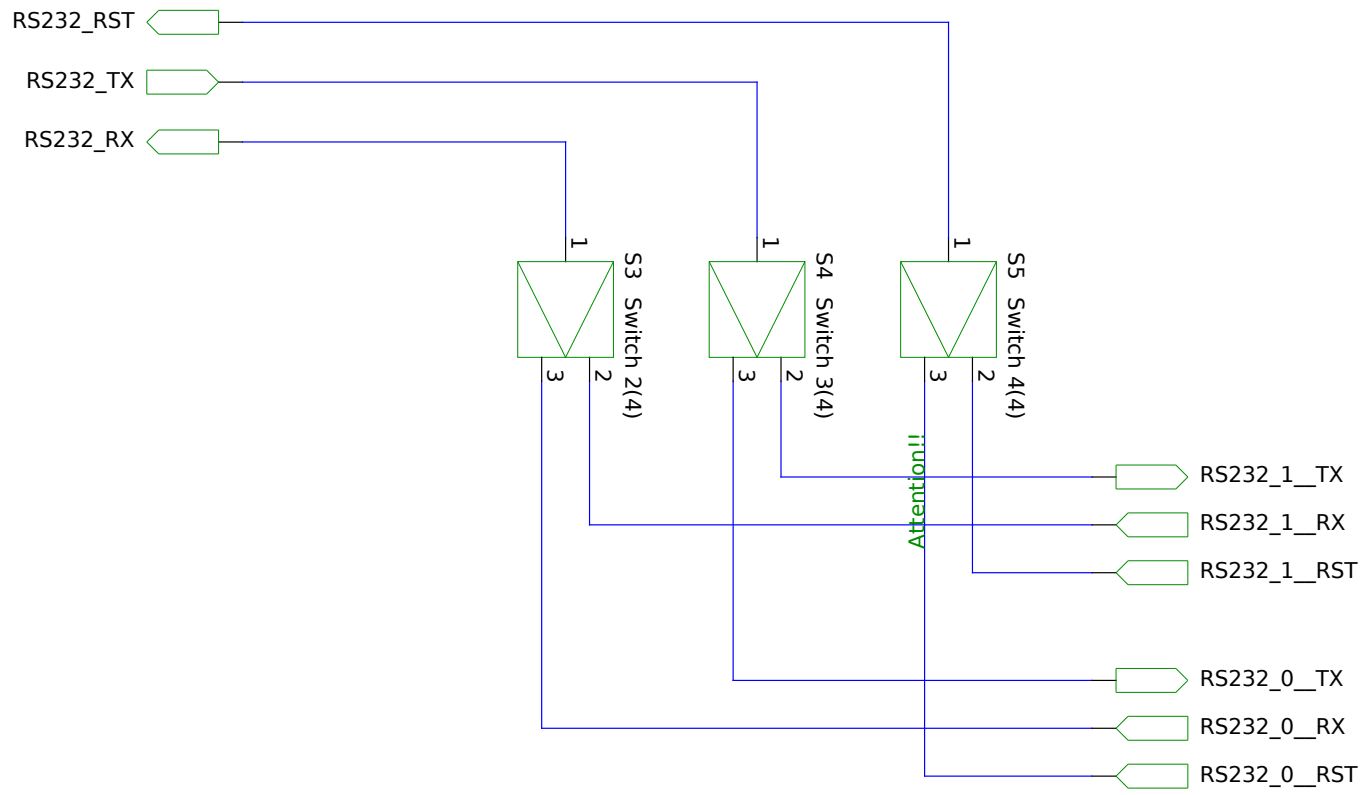
Alternative Serial Port Connection, based on P8X32A-Propeller-Datasheet-v1.4.0\_0.pdf

RS232 Circuit #0		<b>Zerocat Chipflasher</b>	
		Flash free firmware, kick the Management Engine. git://zerocat.org/zerocat/projects/chipflasher	
TITLE		REVISION:	v2.1.1-0-7ab6f93df (master)
FILE:	devkit-page06.sch	DRAWN BY:	kmx
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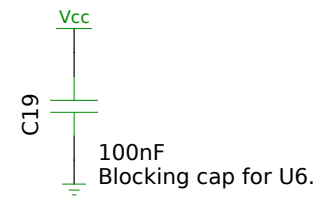
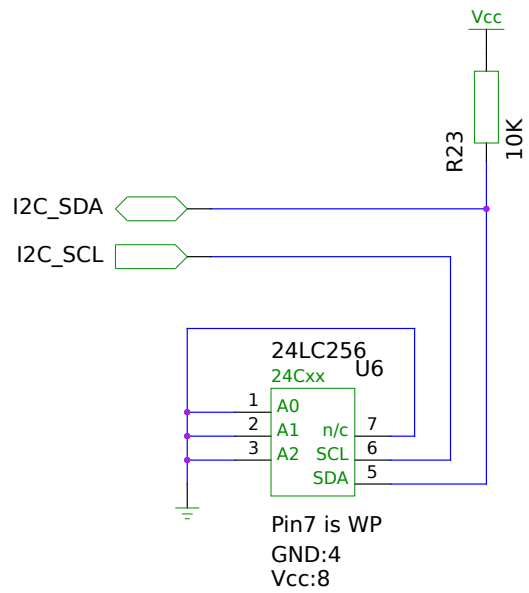
This circuit is generating RS232 voltages of about +/- 5.5VDC.

RS232 Circuit #1		Zerocat Chipflasher	
TITLE		Flash free firmware, kick the Management Engine. <a href="https://zerocat.org/zerocat/projects/chipflasher">git://zerocat.org/zerocat/projects/chipflasher</a>	
FILE:	devkit-page07.sch	REVISION:	v2.1.1-0-7ab6f93df (master)
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<b>RS232 Circuit Selector</b>		<b>Zerocat Chipflasher</b>	
TITLE		Flash free firmware, kick the Management Engine. git://zerocat.org/zerocat/projects/chipflasher	
FILE:	devkit-page08.sch	REVISION:	v2.1.1-0-7ab6f93df (master)
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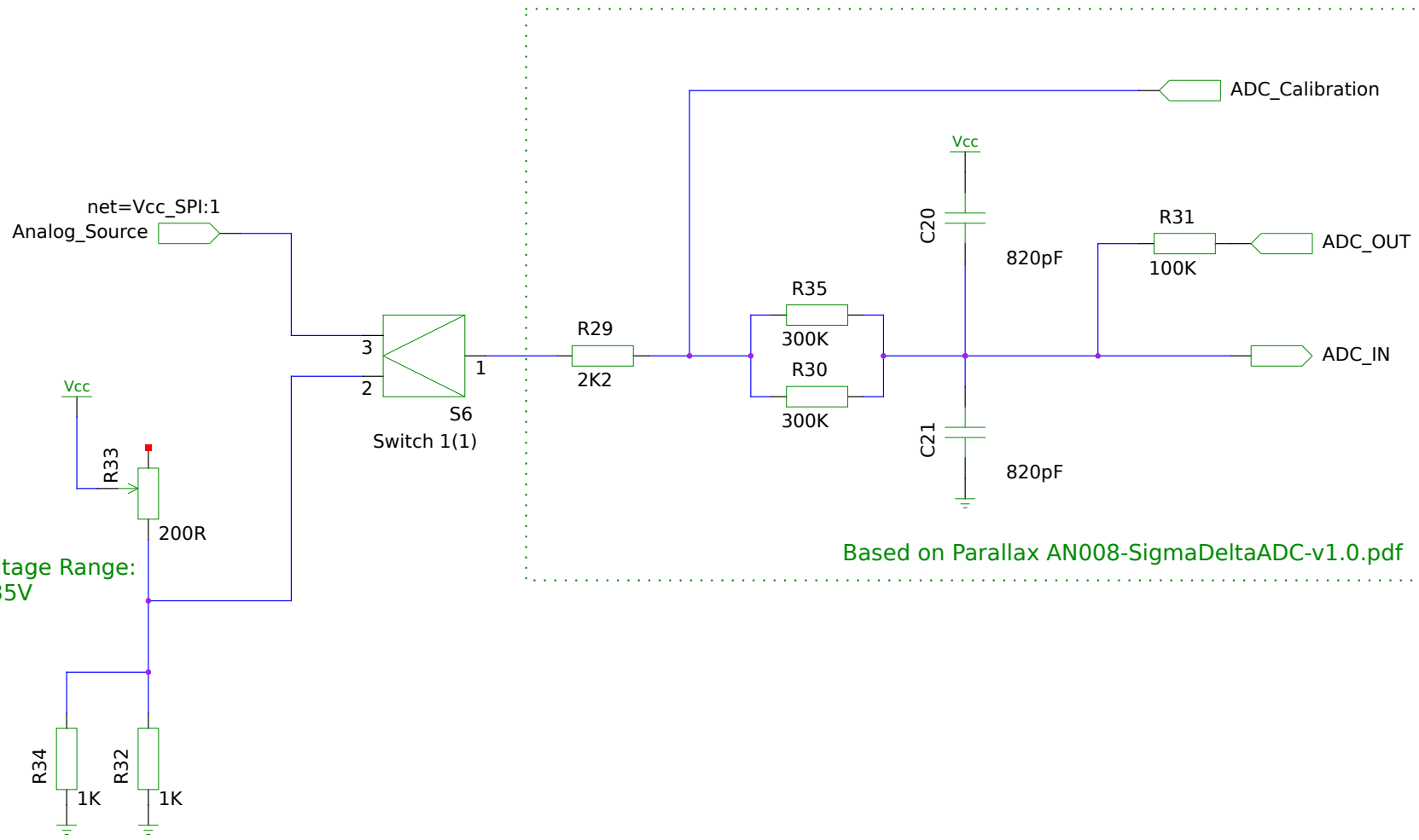




## Serial EEPROM of a non-free Design, optional Zerocat Chipflasher

Flash free firmware, kick the Management Engine.  
[git://zerocat.org/zerocat/projects/chipflasher](https://zerocat.org/zerocat/projects/chipflasher)

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FILE:	devkit-page09.sch	REVISION: v2.1.1-0-7ab6f93df (master)
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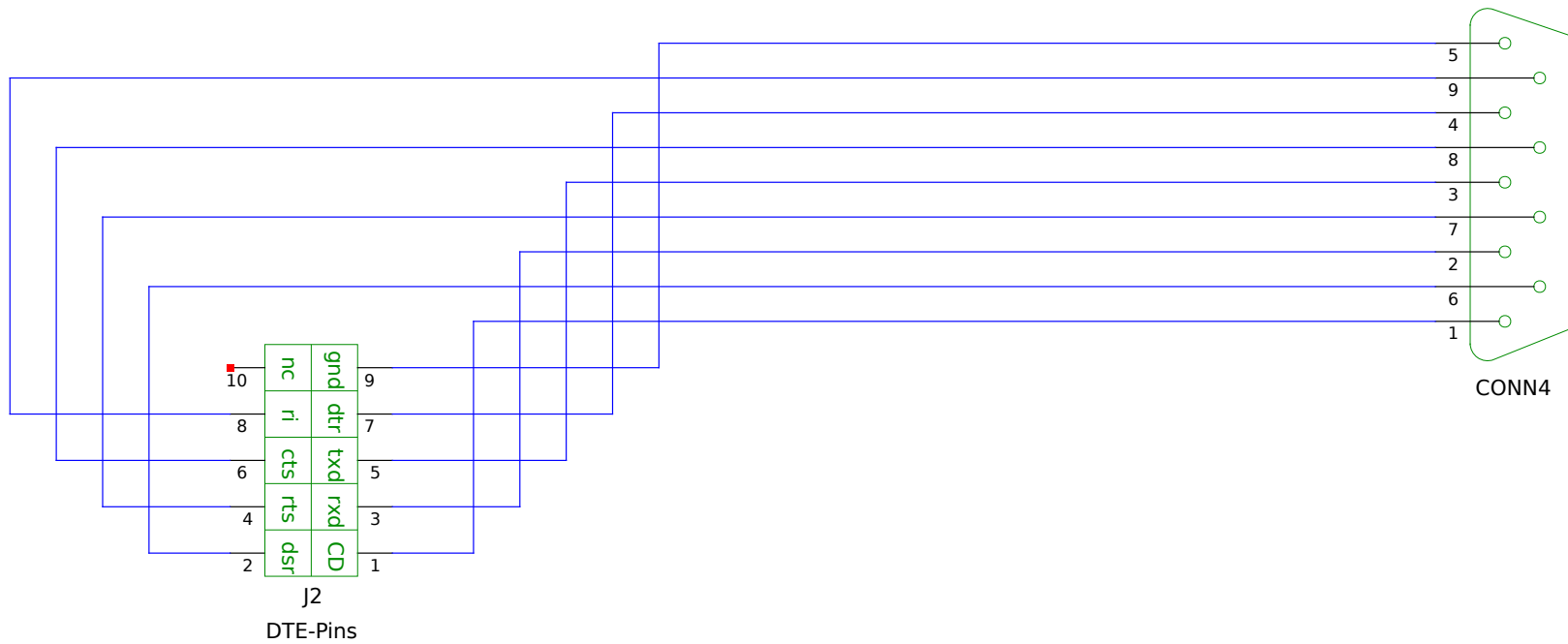
<b>Sigma-delta ADC Circuit</b>		<b>Zerocat Chipflasher</b>	
Flash free firmware, kick the Management Engine.		git://zerocat.org/zerocat/projects/chipflasher	
TITLE		REVISION:	v2.1.1-0-7ab6f93df (master)
FILE:	devkit-page10.sch	DRAWN BY:	kmx
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Note:

DTE = Data Terminal Equipment (Laptop)

DCE = Data Communication Equipment (Propeller)

DTR = Data Terminal Ready (Laptop is ready to send/receive)



RS232 Cable

Zerocat Chipflasher

Flash free firmware, kick the Management Engine.  
[git://zerocat.org/zerocat/projects/chipflasher](https://zerocat.org/zerocat/projects/chipflasher)

TITLE

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devkit-page11.sch

REVISION:

v2.1.1-0-7ab6f93df (master)

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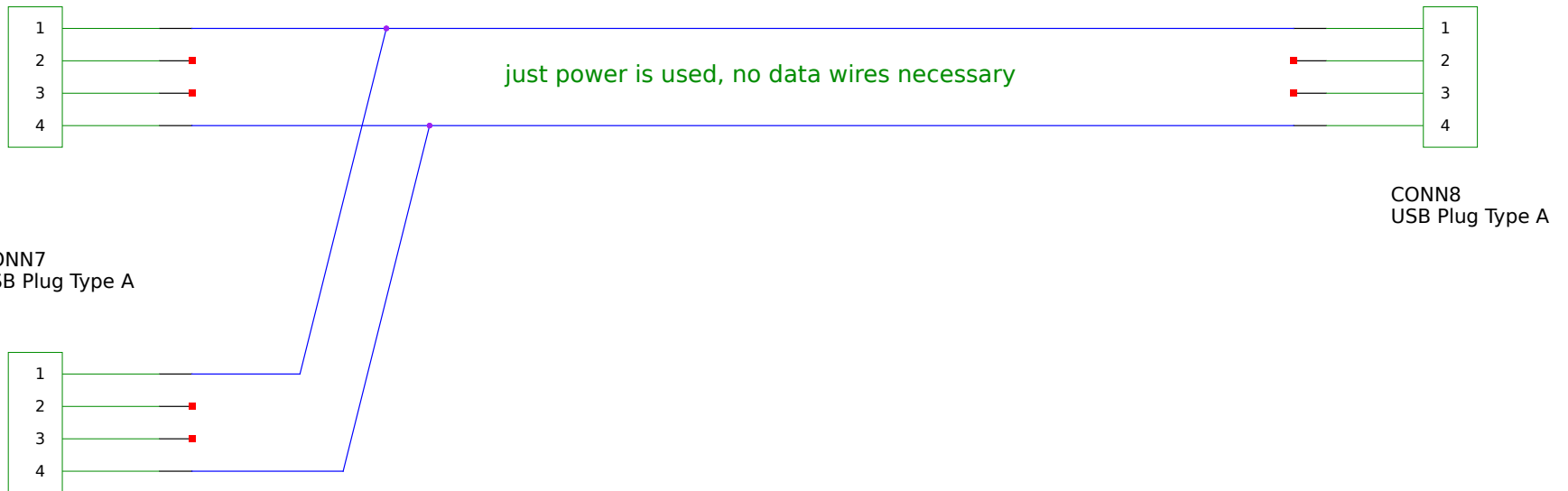
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kmx

CONN6  
USB Plug Type A

Host

Chipflasher

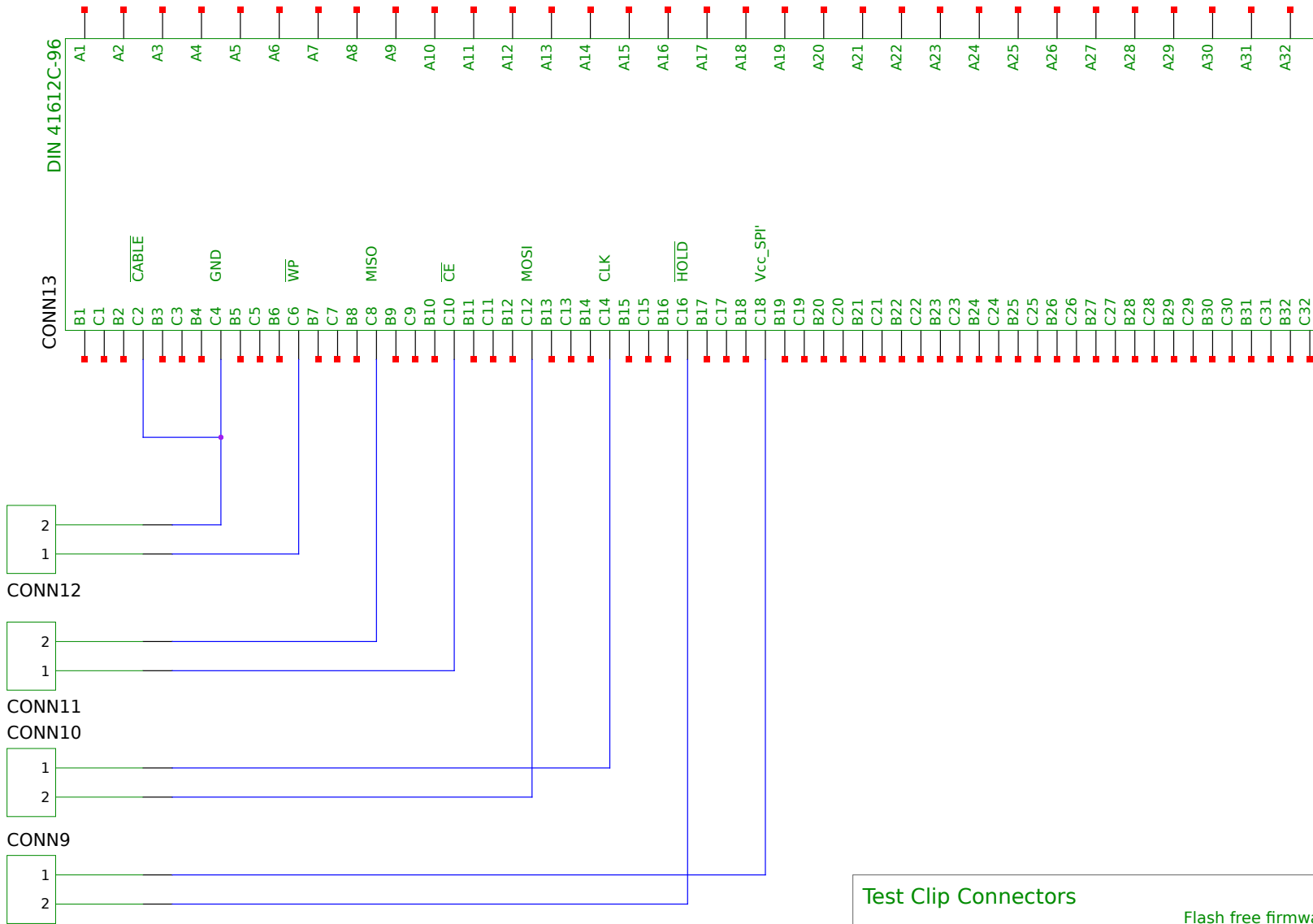


### Y-USB Power Cable

### Zerocat Chipflasher

Flash free firmware, kick the Management Engine.  
[git://zerocat.org/zerocat/projects/chipflasher](https://zerocat.org/zerocat/projects/chipflasher)

TITLE			
FILE:	devkit-page12.sch	REVISION:	v2.1.1-0-7ab6f93df (master)
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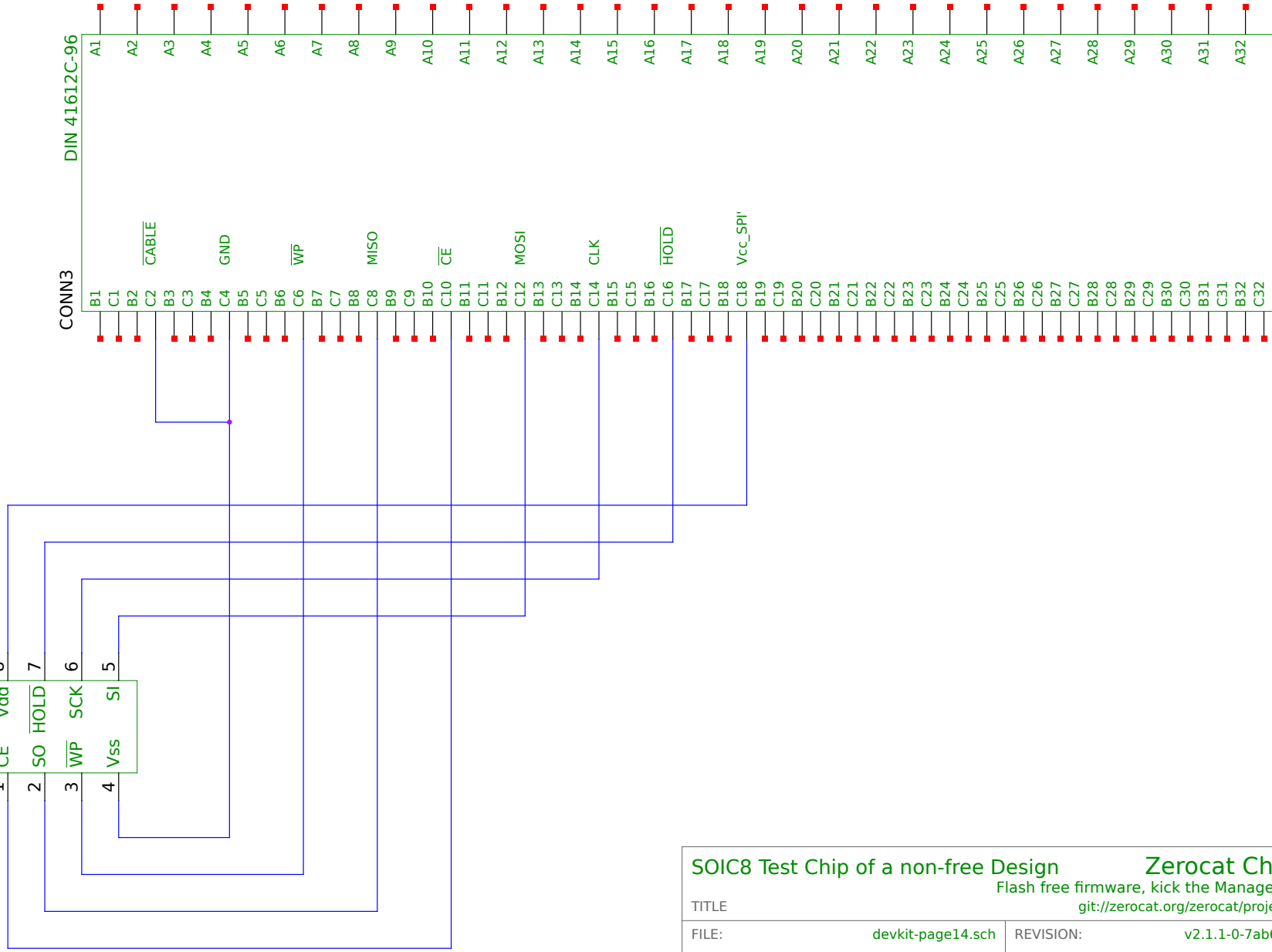
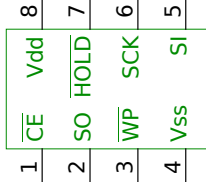


Test Clip Connectors		Zero-cat Chipflasher	
TITLE		Flash free firmware, kick the Management Engine. <a href="https://zerocat.org/zerocat/projects/chipflasher">git://zerocat.org/zerocat/projects/chipflasher</a>	
FILE:	devkit-page13.sch	REVISION:	v2.1.1-0-7ab6f93df (master)
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# SOIC8

Chips with same Pinout:  
M95020W  
MX25L1605D

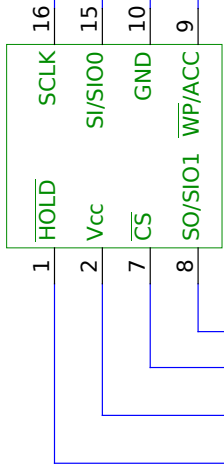
SST25VF016B U8



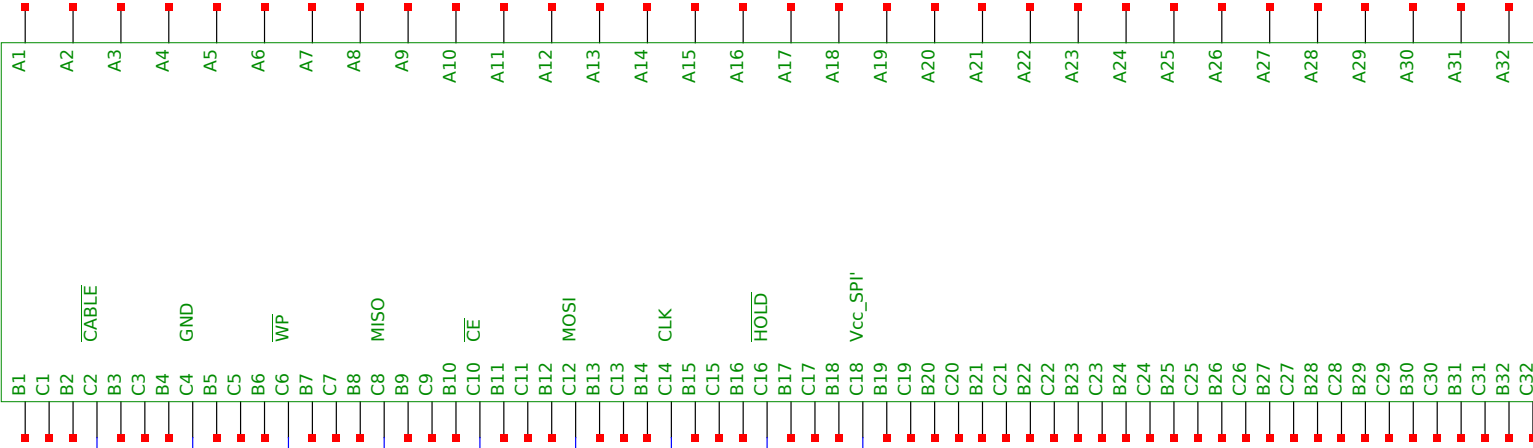
SOIC8 Test Chip of a non-free Design		Zerocat Chipflasher	
Flash free firmware, kick the Management Engine.		git://zerocat.org/zerocat/projects/chipflasher	
TITLE			
FILE:	devkit-page14.sch	REVISION:	v2.1.1-0-7ab6f93df (master)
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# SOIC16

MX25L6405D U10



CONN5  
DIN 41612C-96



## SOIC16 Test Chip of a non-free Design ZeroCAT Chipflasher

Flash free firmware, kick the Management Engine.  
[git://zerocat.org/zerocat/projects/chipflasher](https://zerocat.org/zerocat/projects/chipflasher)

TITLE		
FILE:	devkit-page15.sch	REVISION: v2.1.1-0-7ab6f93df (master)
PAGE	OF	DRAWN BY: kmx