


Chipflasher Label

www.zerocat.org/chipflasher.html	
NC GND RI DTR CTS TXD RTS RXD DSR CD	RS232 DTE Pinout
 Zerocat! Chipflasher v2.1	SPI Pinheader 9x1
WP EEPROM off ↔ on	WP SPI off ↔ on
git://zerocat.org/zerocat/projects/chipflasher.git Licensed under CERN-OHL-S v2 or any later version	
CABLEn GND WPn MISO CEn MOSI CLK HOLDn VSPI '	