


# *board* label

<a href="http://www.zerocat.org/chipflasher.html">www.zerocat.org/chipflasher.html</a>	
NC   GND RI   DTR CTS   TXD RTS   RXD DSR   CD	RS232 DTE Pinout
 <b>ZeroCat!</b> Chipflasher v2	SPI Pinheader 9x1
	#PLUG GND #WP MISO #CE MOSI SCLK #HOLD +3.3V
git://zerocat.org/zerocat/projects/chipflasher Licensed under CERN-OHL-S v2 or any later version	