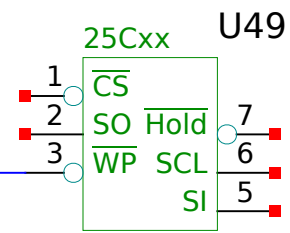
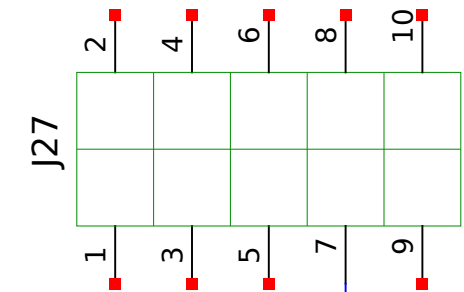
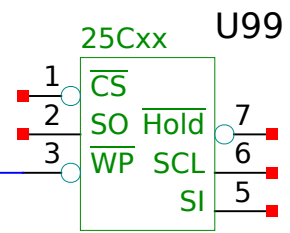


net=GND:4  
net=Vcc\_SPI:8



net=GND:4  
net=Vcc\_SPI:8



## ZeroCAT Flash Access

Interfacing BIOS Chips in Hardware Protection Mode

[git://zerocat.org/zerocat/projects/zerocat-flash-access](https://zerocat.org/zerocat/projects/zerocat-flash-access)

TITLE

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